INVITATION TO
ISMVL 2013 IN TOYAMA, JAPAN

Invitation à symposium international sur logique multivalué, Toyama, Japon

2012/5/15

Takao Waho (Sophia University)
ISMVL 2013

Date: May 22 – May 24, 2013
Venue: Toyama International Conference Center, Toyama, Japan

Post-binary ULSI Workshop
May 21, 2013

Read-Muller Workshop
May 24, 25, 2013
Where is Toyama?

http://www.pref.toyama.lg.jp/

260 km NW of Tokyo
Population: 1,080,000

Small is beautiful, and bigger is not always better.
Access to Toyama

Many Cities from Europe

260 km NW of Tokyo
Population: 1,080,000

Toyama

From America

Narita and Haneda are connected by bus and train (~1.5 h).

Tokyo (Narita)

1 h

6 flights/day 6:55 to 20:00

Tokyo (Haneda)
Access to Toyama

London
Frankfurt
Paris

San Francisco
Los Angeles
Honolulu
New York (JFK)*
(* from June 1st)

260 km NW of Tokyo
Population: 1,080,000

6 flights/day
6:55 to 20:00

1 h

Toyama

Tokyo (Haneda)
To Toyama via Osaka/Nagoya

Frankfurt
Helsinki

Amsterdam
Frankfurt
Helsinki
Paris, Rome

Detroit

New York
San Francisco
Seattle

From Osaka
Kansai Airport
→ (1h10min) → Osaka
→ (3h20min) → Toyama

From Nagoya
Chubu Airport
→ (30min) → Nagoya
→ (4h10min) → Toyama

Plenary Session, ISMVL 2012
2012/5/15
International flights to Toyama

Dalian/Beijing (Daily)

Soul (Tu/F/Su)

Shanghai (Tu/Sa)
Toyama International Conference Center (Venue)

ANA Crowne Plaza Hotel

Single: 15,750 JPY
(~150 EUR, ~180 USD/CAD)

Hotel Route Inn

Single: 6,700 JPY (~65 EUR, 85 USD/CAD)
15-min walk from the Conference Center
(/person, incl. breakfast and tax.)
Symposium Scope

Algebra and Formal Aspects
Logic Programming
Fuzzy Logic and Soft Computing
Machine Learning and Robotics
Logic Design and Switching Theory
Spectral Techniques
VLSI Architecture
System-on-Chip Technology
Medical and Wellness Technology

Automatic Reasoning
Philosophical Aspects
Data Mining
Quantum Computing
Test and Verification
Circuit/Device Implementation
VLSI Computing
Nano Technology

Plenary Session, ISMVL 2012  2012/5/15
Tentative Invited Speakers

Prof. Martin Goldstern
Vienna University of Technology
Universal algebra, Clone, and Set theory

Dr. Hiromitsu Kimura
ROHM Semiconductor, Japan
Nonvolatile logic with FeRAM

Prof. Yutaka Hata
University of Hyogo, Japan
Medical and wellness applications
30th Anniversary of ISMVL in Japan

- Toyama 2013 (43rd ISMVL)
- Sendai 1992
- Fukuoka 1998
- Tokyo 2003
- Kyoto 1983 (13th ISMVL)
- Okinawa 2009

Plenary Session, ISMVL 2012  2012/5/15
Special Issues in Journals:

**Algebra, Logic, and Theory**

*Journal of multiple-valued logic and soft computing*

**Devices, Circuits, and Systems**

*IEICE Transactions on Information and Systems*
Toyama has been selected as one of the “Environment Model Cities” in Japan, and known as “Compact City”.

Famous trout sushi

A moat around the castle

Tram system
Excursion Destination

Gokayama (UNESCO World Heritage Cite; Old thatched houses, Michelin ★★★)
We will have a travel desk to assist you.

Kurobe Gorge
Kuro-Yon Dam
(Tateyama-Kurobe Alpine Route)
News:

Workshop on Post-binary ULSI Systems

May 21, 2013

Poster session with short presentation

Please encourage your students to submit papers to the poster session.
Committee Members

Symposium Chair: Takao Waho (Sophia Univ.)
Symposium Vice-Chair: Takahiro Hanyu (Tohoku Univ.)
Program Chair: Yasushi Yuminaka (Gunma Univ.)
Local Arrangement Chair: Noboru Takagi (Toyama Pref. Univ.)
Registration and Financial Chair: Koichi Tanno (Univ. Miyazaki)
Publicity Chair: Takahiro Hozumi (Hyogo Univ.)
Publication Chair: Shinobu Nagayama (Hiroshima City Univ.)
Advisory Committee Chair: Michitaka Kameyama (Tohoku Univ.)
Post-Binary ULSI Workshop: Naofumi Homma (Tohoku Univ.)
Reed-Muller Workshop: Tsutomu Sasao (Kyushu Inst. Tech.)

We are very much looking forward to seeing you in Toyama!
The Multiple-Valued Logic Technical Committee of the IEEE Computer Society will hold its 43rd annual symposium ISMVL 2013 in Toyama, Japan (Toyama International Conference Center), on May 22-24, 2013.

The event is sponsored by the IEEE Computer Society and Japan MVL Research Group.

You are invited to submit an original paper, survey or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

- Algebra and Formal Aspects
- Automatic Reasoning
- Logic Programming
- Philosophical Aspects
- Fuzzy Logic and Soft Computing
- Data Mining
- Machine Learning and Robotics
- Quantum Computing
- Logic Design and Switching Theory
- Test and Verification
- Spectral Techniques
- Circuit/Device Implementation
- VLSI Architecture and Computing
- System-on-Chip Technology
- Nano Technology
- Medical and Wellness Technology