

# INVITATION TO ISMVL 2013 IN TOYAMA, JAPAN

Invitation à symposium international sur  
logique multivalué, Toyama, Japon

2012/5/15

Takao Waho (Sophia University)

# ISMVL 2013



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Date: May 22 – May 24, 2013

Venue: Toyama International Conference Center,  
Toyama, Japan

Post-binary ULSI Workshop

May 21, 2013

Read-Muller Workshop

May 24, 25, 2013

May 2013						
Mo	Tu	We	Th	Fr	Sa	Su
		1	2	3	4	5
6	7	8	9	10	11	12
13	14	15	16	17	18	19
20	21	22	23	24	25	26
27	28	29	30	31		

# Where is **Toyama**?

<http://www.pref.toyama.lg.jp/>

260 km NW of Tokyo  
Population: 1,080,000

Toyama

Small is beautiful, and  
bigger is not always better.

Tokyo

# Access to **Toyama**

Many Cities  
from Europe

260 km NW of Tokyo  
Population: 1,080,000

**Toyama**

From America

Narita and Haneda are  
connected by bus and  
train (~1.5 h).

Tokyo  
(Narita)

Tokyo  
(Haneda)

6 flights/day  
6:55 to 20:00

1 h

# Access to **Toyama**

London  
Frankfurt  
Paris

260 km NW of Tokyo  
Population: 1,080,000

Toyama

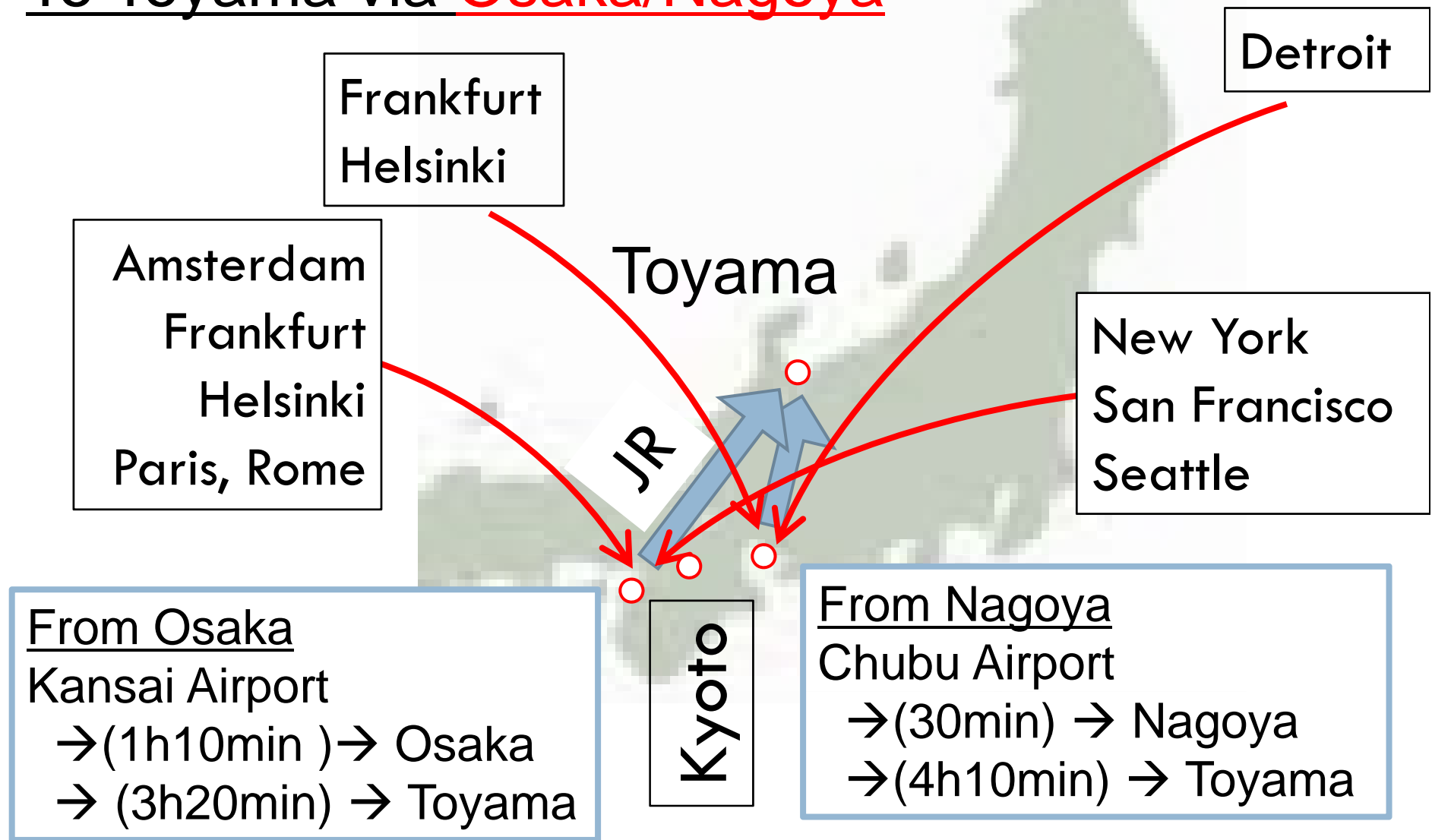
San Francisco  
Los Angeles  
Honolulu  
New York (JFK)\*  
(\* from June 1st)

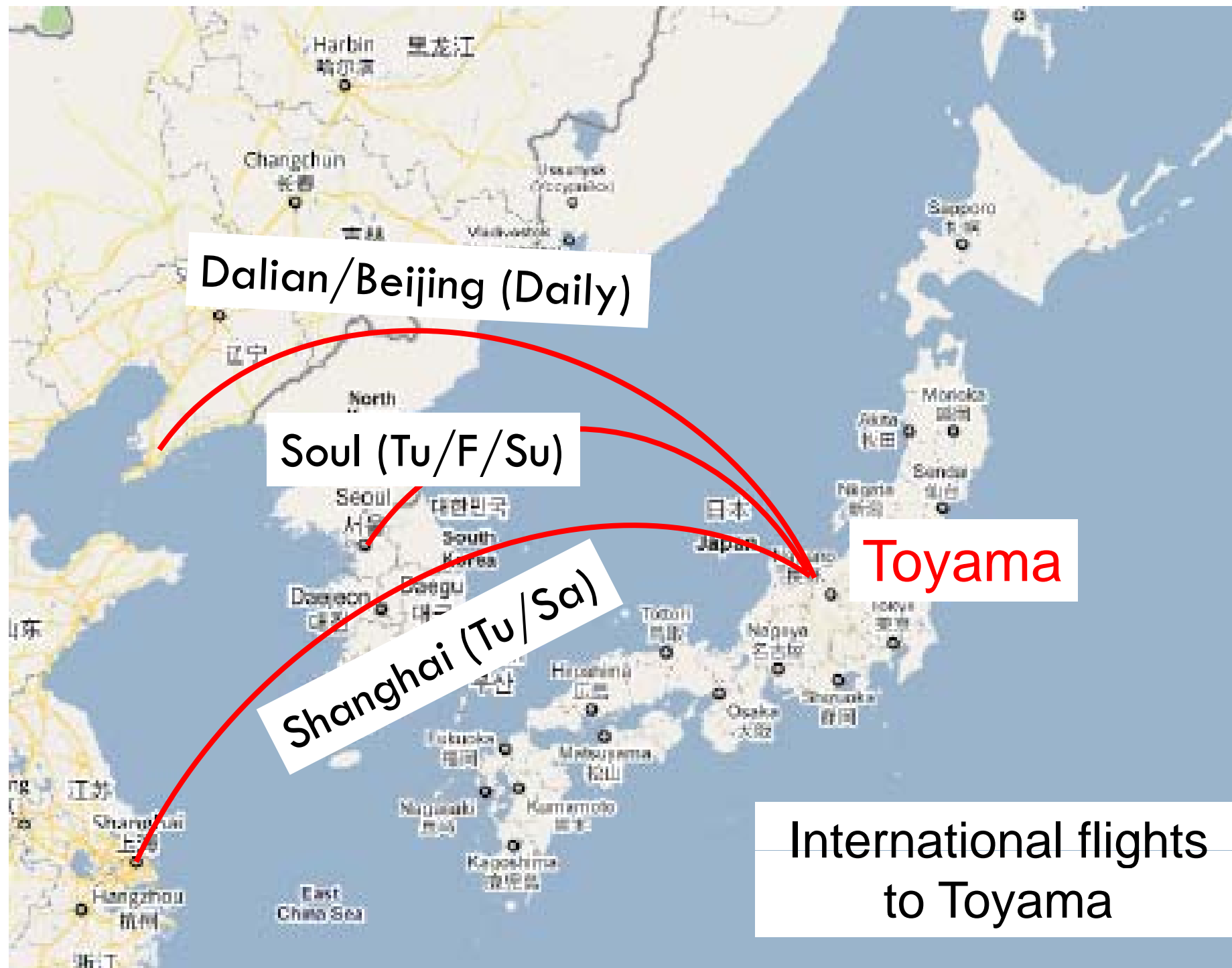
6 flights/day  
6:55 to 20:00

Tokyo  
(Haneda)

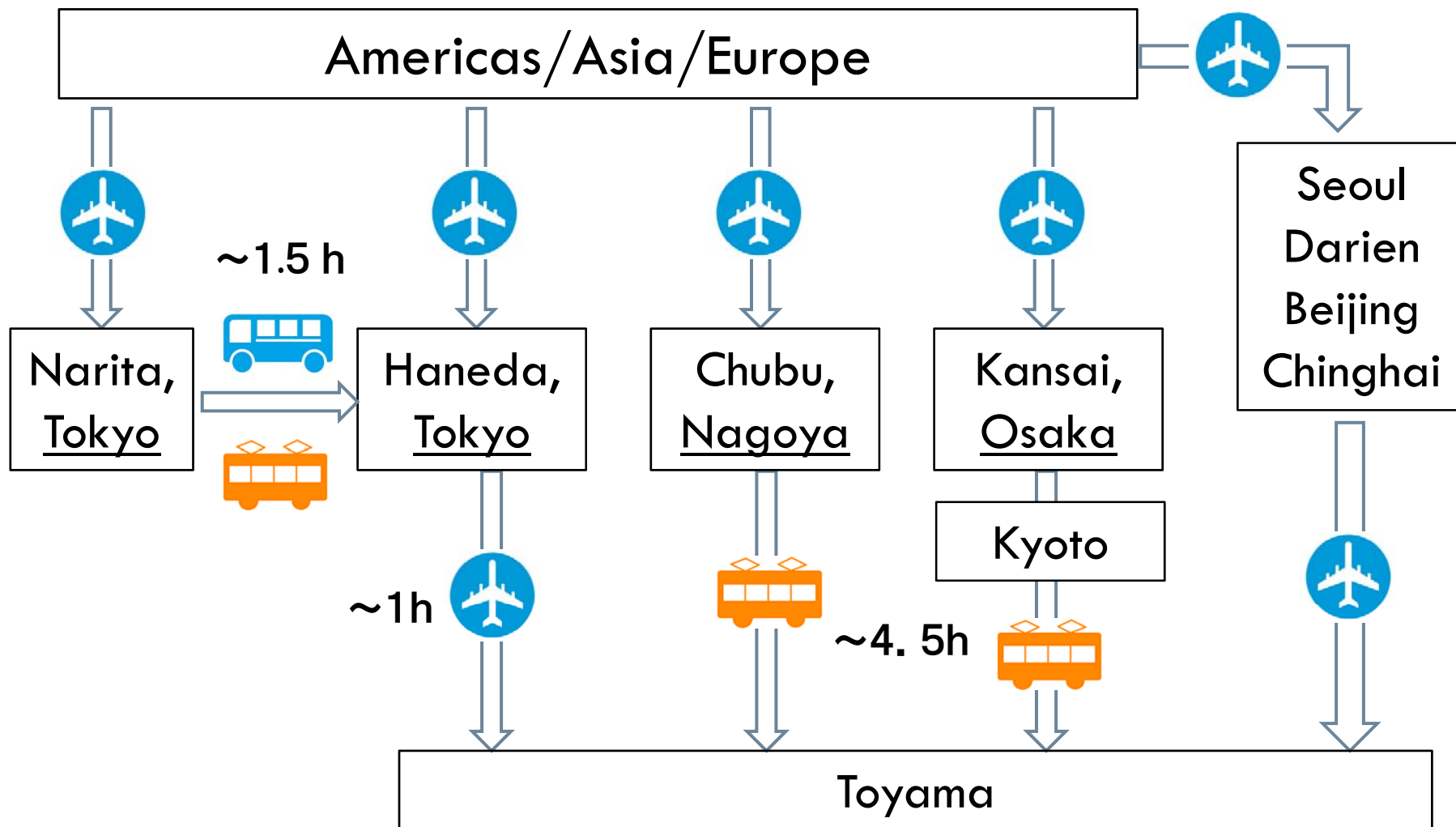
1 h

# To Toyama via Osaka/Nagoya





International flights  
to Toyama





from Osaka/Kyoto  
and Nagoya



JR Toyama St.

Hotel Route Inn

Toyama  
International  
Conference Center  
(Venue)

ANA  
Crowne Plaza  
Hotel



~ 30 min from  
Toyama Airport

ANA  
Crowne Plaza Hotel

Toyama International  
Conference Center (Venue)



Single: 15,750 JPY  
(~150 EUR,  
~180 USD/CAD)

Hotel Route Inn

Single: 6,700 JPY (~65 EUR, 85 USD/CAD)  
15-min walk from the Conference Center  
(/person, incl. breakfast and tax.)

# Symposium Scope

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Algebra and Formal Aspects

Logic Programming

Fuzzy Logic and Soft Computing

Machine Learning and Robotics

Logic Design and Switching Theory

Spectral Techniques

VLSI Architecture

System-on-Chip Technology

**Medical and Wellness Technology**

Automatic Reasoning

Philosophical Aspects

Data Mining

Quantum Computing

Test and Verification

Circuit/Device Implementation

VLSI Computing

Nano Technology

# Tentative Invited Speakers

12

**Prof. Martin Goldstern**

*Vienna University of Technology*

Universal algebra, Clone, and Set theory

**Dr. Hiromitsu Kimura**

*ROHM Semiconductor, Japan*

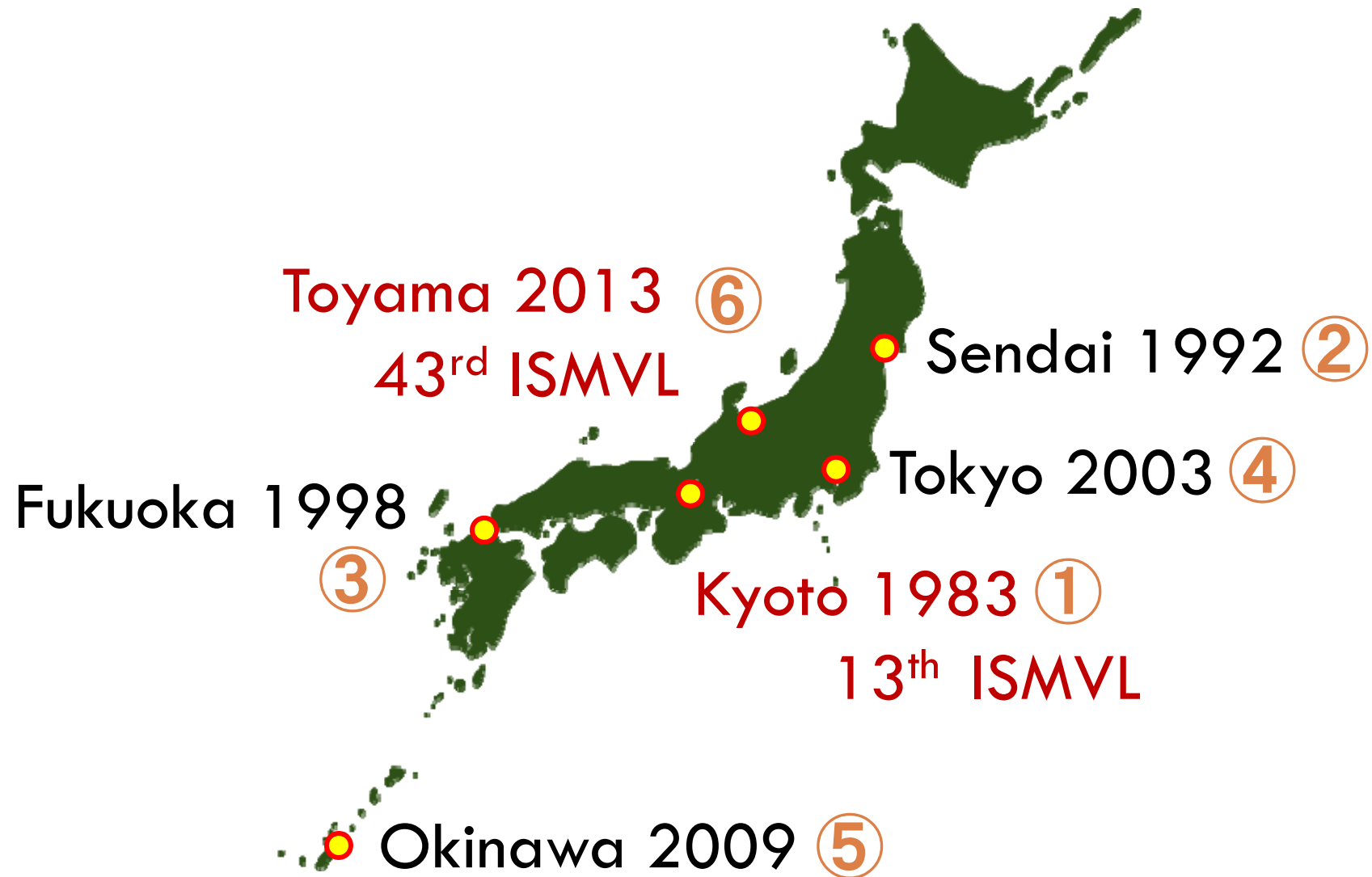
Nonvolatile logic with FeRAM

**Prof. Yutaka Hata**

*University of Hyogo, Japan*

Medical and wellness applications

# 30<sup>th</sup> Anniversary of ISMVL in Japan



## Special Issues in Journals:

### Algebra, Logic, and Theory

*Journal of multiple-valued logic and soft computing*

### Devices, Circuits, and Systems

*IEICE Transactions on Information and Systems*



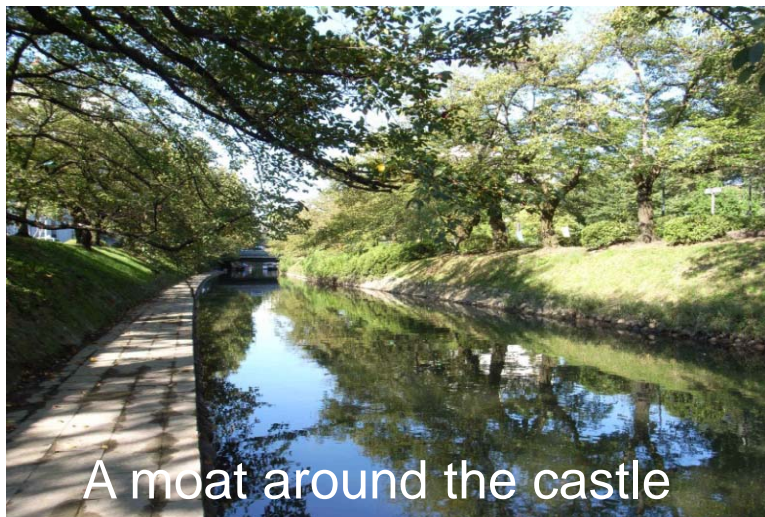


Toyama castle



Famous trout sushi

Toyama has been selected as one of the “Environment Model Cities” in Japan, and known as “Compact City”.



A moat around the castle



Tram system





## Excursion Destination



Gokayama (UNESCO World Heritage Site;  
Old thatched houses, Michelin ★★★)



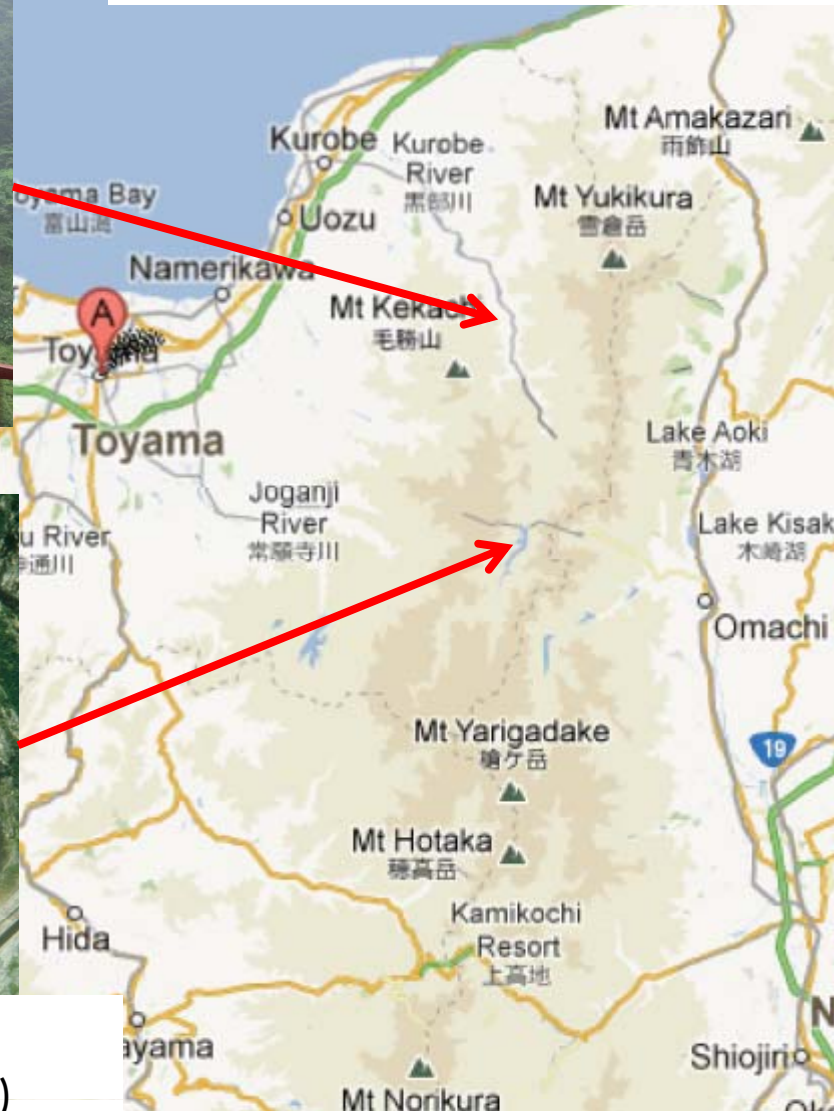
We will have a travel desk  
to assist you.



Kurobe Gorge



Kuro-Yon Dam  
(Tateyama-Kurobe Alpine Route)



News:

## Workshop on Post-binary ULSI Systems

May 21, 2013

Poster session with short presentation

Please encourage your students to submit papers to the poster session.

# Committee Members

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Symposium Chair:	<i>Takao Waho</i> (Sophia Univ.)
Symposium Vice-Chair:	<i>Takahiro Hanyu</i> (Tohoku Univ.)
Program Chair:	<i>Yasushi Yuminaka</i> (Gunma Univ.)
Local Arrangement Chair:	<i>Noboru Takagi</i> (Toyama Pref. Univ.)
Registration and Financial Chair:	<i>Koichi Tanno</i> (Univ. Miyazaki)
Publicity Chair:	<i>Takahiro Hozumi</i> (Hyogo Univ.)
Publication Chair:	<i>Shinobu Nagayama</i> (Hiroshima City Univ.)
Advisory Committee Chair:	<i>Michitaka Kameyama</i> (Tohoku Univ.)
Post-Binary ULSI Workshop:	<i>Naofumi Homma</i> (Tohoku Univ.)
Reed-Muller Workshop:	<i>Tsutomu Sasao</i> (Kyushu Inst. Tech.)

**We are very much looking forward to seeing you in Toyama!**

# IEEE 43rd International Symposium on Multiple-Valued Logic ISMVL-2013, May 21-24, 2013, Toyama, Japan

22nd International Workshop on Post-Binary ULSI Systems, May 21, 2013  
Reed-Muller 2013 Workshop, May 24-25, 2013

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- [Organization and other info.](#)



## Conference Sponsors



[TC on Multiple-Valued Logic](#)



Please visit our web page!

## Latest

The [Multiple-Valued Logic Technical Committee](#) of the IEEE Computer Society will hold its 43rd annual symposium ISMVL2013 in Toyama, Japan ([Toyama International Conference Center](#)), on **May 22-24, 2013**

The event is sponsored by the [IEEE Computer Society](#) and [Japan MVL Research Group](#).

You are invited to submit an original paper, survey or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

Algebra and Formal Aspects  
Automatic Reasoning  
Logic Programming  
Philosophical Aspects  
Fuzzy Logic and Soft Computing  
Data Mining  
Machine Learning and Robotics  
Quantum Computing

Logic Design and Switching Theory  
Test and Verification  
Spectral Techniques  
Circuit/Device Implementation  
VLSI Architecture and Computing  
System-on-Chip Technology  
Nano Technology  
Medical and Wellness Technology