



ISMVL 2017

May 22 – 24, 2017, Novi Sad, Serbia

Call for Participation



The IEEE Computer Society's Technical Committee on Multiple-Valued Logic will hold its 47th annual symposium (ISMVL 2017) at University of Novi Sad in Novi Sad, Serbia, from May 22 to 24, 2017. ISMVL 2017 offers you a great opportunity to follow the recent technologies and explore future directions in multiple-valued logic and its related areas.

The following keynote speakers will present their cutting-edge research results:

- **Dmitriy Zhuk** (Moscow State University, RUSSIA)
An Algorithm for Constraint Satisfaction Problem
- **Branislav Kisačanin** (Nvidia Corporation, USA)
Deep Learning for Autonomous Vehicles
- **Tsutomu Sasao** (Meiji University, JAPAN)
Index Generation Functions: Minimization Methods

Technical (Regular) Papers:

Other than the invited talks, 45 high-quality papers will be presented as well. The program is available at <http://mvl.jp.org/ISMVL2017/>

Accompanying Workshop:

In conjunction with ISMVL 2017, the 26th International Workshop on Post-Binary ULSI Systems (May 21) and the Reed-Muller 2017 workshop (May 24-25) will be held at the same location.

Registration:

All participants are required to register. The deadline for early registration is **April 10, 2017**. Details about registration such registration fee and how to register are available at: <http://mvl.jp.org/ISMVL2017/>

If you have any question, please contact:

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