ISMVL 2021

May 25 – 27, 2021, **Fully Virtual Symposium**

Call for Participation

The IEEE Computer Society's Technical Committee on Multiple-Valued Logic will hold its 51st annual symposium in **fully online**, on May 25 – 27, 2021. ISMVL 2021 offers you a great opportunity to follow the recent technologies and explore future directions in multiple-valued logic and its related areas.
The following invited speakers will present their cutting-edge research results:

- **Prof. Andrew Adamatzky** (University of the West of England Bristol, United Kingdom)
  Unconventional Computing
- **Prof. Nobuyuki Matsuda** (Tohoku University, Japan)
  Quantum Information Processing Using Integrated Photonics
- **Prof. Libor Barto** (Charles University, Czech Republic)
  Minimal Taylor Clones

Technical (Regular) Papers:
Other than the invited talks, 35 high-quality papers will be presented as well. The program will be available soon at
http://mvl.jpn.org/ISMVL2021/

Accompanying Workshops:
In conjunction with ISMVL 2021, the 30th International Workshop on Post-Binary ULSI Systems and the Reed-Muller 2021 Workshop will be held on May 28, 2021.

Registration:
All participants are required to register. The deadline for early registration is April 23, 2021. Details about registration such as registration fee and how to register are available at:
http://mvl.jpn.org/ISMVL2021/registration.php

If you have any question, please contact:

Symposium Chair:	**Program Chair:**
Prof. Martin Lukac	**Prof. Shinobu Nagayama**
Nazarbayev University, Kazakhstan	Hiroshima City University, Japan
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