Journal of Applied Logics (JALs) Special Issue on Multiple-Valued Logic and Applications

CALL FOR PAPERS

The <u>Journal of Applied Logics (JALs-ifCoLog)</u> will publish a special issue on Multiple-Valued Logic and Applications. You are invited to submit an article on any subject in the area of multiple-valued logic, including but not limited to:

Circuit/Device Implementation
Multiple-Valued Decision Diagrams
Quantum Computing
Test and Verification
Brain Scale Integration Circuits and Computing
Application of MVL to Medical Technologies, robotics, machine learning

Multi-bit per Cell Logic & Memory
MVL Aspects of Nanotechnology
System-on-Chip Technology
VLSI Architecture and Computing
Reversible, Stochastic Computing
Spectral Techniques

Algebra and Formal Aspects

MVL approaches to Data Mining and Big Data

Articles and topics should be addressed primarily to the diverse readership of *JALs-ifCoLog*. Unpublished and original work is preferred, however extended articles from IEEE ISMVL 2021 can be submitted as well. Articles should target novel and emerging end-applications of multiple-valued logic. Authors should submit papers by using the <u>guidelines</u> and <u>template</u> and sending pdf files directly to <u>martin.lukac@nu.edu.kz</u> or to <u>s_naga@hiroshima-cu.ac.jp</u>. Each manuscript should follow <u>guidelines</u> in the guide for authors. This special issue will follow the schedule below:

August 15, 2021 November 30, 2021 January 10, 2022 January 30, 2022 February 15, 2022 March 2022 Due date for papers
First notification to authors
Due date for revisions
Final notification
Final manuscript due
Special issue publication date

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