54th IEEE International Symposium on Multiple-Valued Logic

ISMVL 2024

May 28–30, 2024, Brno, Czech Republic

Call for Papers







The IEEE Computer Society's Technical Community on Multiple-Valued Logic will hold its 54th annual symposium in Brno, Czech Republic, on May 28–30, 2024. You are invited to submit an original paper, survey, or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

Algebra and Formal Aspects Boolean Satisfiability Automatic Reasoning Communication Systems Logic Programming Philosophical Aspects of MVL Fuzzy Systems and Soft Computing Data Mining Machine Learning and Robotics Quantum Computing Quantum Cryptography Reversible Computing Logic Design and Switching Theory Automatic Test Pattern Generation Test and Verification Computer Arithmetic Spectral Techniques Circuit/Device Implementation Image Processing Signal Processing Mathematical Fuzzy Logic MVL in Nanotechnology MVL in Medical Technologies MVL in Data Science and Analytics

Authors should submit papers in PDF format by **November 1, 2023** via the symposium website. Each manuscript should include a 50-to-100 word abstract, and should not exceed 6 pages in the 2-column IEEE Proceedings format. The proceedings of ISMVL are part of Ei Compendex being the broadest and most complete engineering literature database available in the world. Special issues of some journals are planned for selected ISMVL 2024 papers.

Authors will be notified about acceptance by **February 1, 2024**. Camera-ready copies of accepted papers are due on **March 1, 2024**. For submission details, please visit the symposium website:

https://mvl.jpn.org/ISMVL2024/

The symposium will be co-located with the 33rd International Workshop on Post-Binary ULSI Systems. Information on accommodations and transportation to Brno will be provided at a later date- This event is sponsored by the IEEE Computer Society. For additional information, please contact:

Symposium Chair: Jan Paseka Masaryk University, Czech Republic paseka@math.muni.cz **Programme Chairs: Mike Behrisch** TU Wien, Austria behrisch@logic.at

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