53rd IEEE International Symposium on Multiple-Valued Logic

ISMVL 2023

May 22 - 24, 2023, Kunibiki Messe, Matsue, Shimane, Japan

Final Program

















Supported by:











	Sunday, May 21, 2023 (JST)	
14:30	Workshop on Post-Binary ULSI Systems	@ Small Hall (SH)
	Workshop Chair: Yosuke lijima	
17:00	Welcome Reception	@ Multipurpose Hall (MH)

	Monday, May 22, 2	2023 (JST)
08:45	Opening Symposium Chair: Yasushi Yuminaka & Program Cha	@ MH air: Naofumi Homma
09:00	[Keynote Address I] Chair: Hiroki Nakahara Tackling the Explosions of Data and Solutions with Prof. Masato Motomura (Tokyo Institute of Technology)	@ MH h Low-Bitwidth Computing Architectures
10:00	Coffee Break	
10:20	[Session 1A: Machine Learning Circuits] @ MH Chair: Yosuke lijima	[Session 1B: Non Classical Logics] @ SH Chair: <i>Hajime Machida</i>
10:20	A Consideration on Ternary Adversarial Generative Networks K. Nakamura and H. Nakahara	Kalmbach Implication in Orthomodular Posets K. Emir and J. Paseka
10:45	Write-Energy Relaxation of MTJ-Based Quantized Neural-Network Hardware K. Asano, M. Natsui, and T. Hanyu	Natural Deduction with Explosion and Excluded Middle N. Kamide
11:10	Easily Reconstructable Logic Functions T. Sasao	Self-extensional Paradefinite Four-valued Modal Logic Compatible with Standard Modal Logic N. Kamide
11:35		An Inductive Construction for Many-Valued Coalgebraic Modal Logic CY. Lin and CJ. Liau
12:00	Lunch (Symposium & Executive Committee Meeting)	@ MH

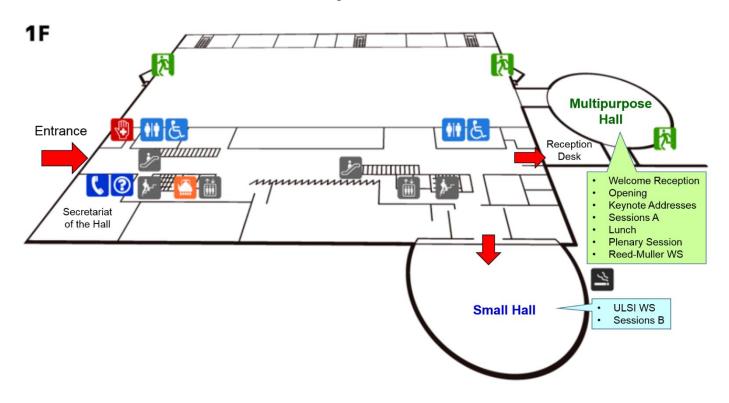
	Monday, May 22, 2023 (、	JST) (continued)
13:20	[Session 2A: Medical & Healthcare] @ MH Chair: Yukihiro Iguchi	[Session 2B: Function Representation & Transformation] @ SH Chair: Rei Ueno
13:20	On Neural-Network-Based Detection for Hypertensive Subjects Using Classification of Retinal Fundus Photographs Y. Sonetsuji, T. Isokawa, N. Kamiura, and H. Tabuchi	Properties of the Reed-Muller-Fourier Spectra of Maiorana-McFarland Bent Functions C. Moraga, R. Stanković, and M. Stanković
13:45	Predicting the Development of Chronic Lung Disease in Neonates from Chest X-ray Images Using Deep Learning R. Maeda, D. Fujita, and S. Kobashi	Remarks on Gibbs Permutation Matrices for Ternary Bent Functions R. Stanković, M. Stanković, C. Moraga, and J. Astola
14:10	Kidney Tumor Recognition from Abdominal CT Images using Transfer Learning S. Wasi, S. B. Alam, R. Rahman, M. A. Amin, and S. Kobashi	Decomposition-Based Representation of Symmetric Multiple-Valued Functions S. Nagayama, T. Sasao, and J. Butler
14:35	Detection of Osteochondritis Dissecans Using Convolutional Neural Networks for Computer-aided Diagnosis of Baseball Elbow K. Sasaki, D. Fujita, K. Takatsuji, Y. Kotoura, M. Minami, Y. Kobayashi, T. Sukenari, Y. Kida, K. Takahashi, and S. Kobashi	Logic Synthesis from Polynomials with Coefficients in the Field of Rationals B. Sampathkumar, B. Martin, R. Das, P. Kalla, and F. Enescu
15:00	Coffee Break	
15:20	[Session 3A: Signal/Data Processing] @ MH Chair: Masanori Natsui	[Session 3B: Algebra & Clone] @ SH Chair: Jovanka Pantović
15:20	Delta-Sigma Domain Signal Processing: A Review with Relevant Topics in Stochastic Computing T. Waho, A. Koyama, and H. Hayashi	Kleene Algebra With Tests for Weighted Programs I. Sedlar
15:45	PAM-4 Data Transmission Quality Evaluation Using Two- and Three-Dimensional Mapping of Received Symbols Y. Yuminaka, K. Nakajima, and Y. lijima	
16:10	Evaluation and Symbol Classification of Multi-Valued Signaling Using Two-Dimensional Symbol Mapping with Linear Mixture Model Y. lijima, K. Nakajima, and Y. Yuminaka	Search for Some Majority Operation and Studies of its Centralizing Monoid H. Machida
16:35	Data Mining Using Multi-Valued Logic Minimization T. Sasao	Weak Bases for Maximal Clones M. Behrisch

	Tuesday, May 23,	2023 (JST)
09:00	[Keynote Address II] Chair: Yasushi Yuminaka	@ MH
	A Challenge of Scalable Quantum Computing Con	trol Systems
	Prof. Takefumi Miyoshi (QuEL, Inc./e-trees. Japan, Inc.	c./QIQB, Osaka University, Japan)
10:00	Coffee Break	
10:20	[Session 4A: Quantum Circuits] @ MH	[Session 4B: SAT Solvers] @ SH
	Chair: Mitchell Thornton	Chair: Tsutomu Sasao
10:20	Towards an Automated Framework for Realizing	Benchmarking Łukasiewicz Logic Solvers with
	Quantum Computing Solutions	Properties of Neural Networks
	N. Quetschlich, L. Burgholzer, and R. Wille	S. Preto, F. Manyà, and M. Finger

	Tuesday, May 23, 2023 ((JST) continued
10:45	Optimized Density Matrix Representations:	Linking Łukasiewicz Logic and Boolean
	Improving the Basis for Noise-Aware Quantum	Maximum Satisfiability
	Circuit Design Tools	S. Preto, F. Manyà, and M. Finger
	T. Grurl, J. Fuß, and R. Wille	
11:10	Using S Gates and Relative Phase Toffoli Gates	A Tableau Calculus for Signed Maximum
	to Improve T-Count in Quantum Boolean Circuits	Satisfiability
	D. Clarino, S. Kuroda, and S. Yamashita	S. Li, J. Coll, D. Habet, CM. Li, and F. Manyà
11:35	Quick Computation of the Lower Bound on the	From Ramon Liull To Lov Grover: Towards A
	Gate Count of Toffoli-Based Reversible Logic	Universal Logic Machine
	Circuits	G. Opsahl and M. Perkowski
	T. Hirayama, R. Suzuki, K. Yamanaka, and Y.	
	Nishitani	
12:00	Short Break	
12:30	Excursion (Lunch on the bus)	
18:30	Banquet at the Shimane Art Museum	

	Wednesday, May 24	-, 2023 (JST)
09:00	[Keynote Address III] Chair: Naofumi Homma Card-based Cryptography: How to Securely Con Cards Prof. Takaaki Mizuki (Tohoku University, Japan)	@ MH npute Multiple-valued Functions Using a Deck of
10:00	Coffee Break	
10:20	[Session 5A: Security] @ MH Chair: Shinobu Nagayama	[Session 5B: Emerging Applications] @ SH Chair: Ryoichi Miyauchi
10:20	Multiple-Valued Logic Physically Unclonable Function in Photonic Integrated Circuits D. MacFarlane, H. Shahoei, I. Achu, E. Stewart, W. Oxford, and M. Thornton	A Logical Method to Predict Outcomes After Coronary Artery Bypass Grafting T. Sasao, A. Holmgren, and P. Eklund
10:45	Higher-Order Boolean Masking Does Not Prevent Side-Channel Attacks on LWE/LWR-based PKE/KEMs K. Ngo, R. Wang, E. Dubrova, and N. Paulsrud	Discovering Emerging Applications of Multi-Valued Logic: Protocols for Human-Autonomy Teaming P. Shmerko, M. Perkowski, Y. Iwashita, A. Stoica, and S. Yanushkevich
11:10	Efficient DFA-Resistant AES Hardware Based on Concurrent Fault Detection Scheme R. Ueno, Y. Yagyu, and N. Homma	Some Consistency Criteria for Many-Valued Judgment Aggregation C. Fermüller
11:35	Plenary Session & Closing TCMVL Chair: Jovanka Pantović & Symposium Chair:	@ MH : Yasushi Yuminaka
12:20	Lunch	@ MH
13:20	Reed-Muller 2023 Workshop Workshop Chair: <i>Tsutomu Sasao</i>	@ MH

Brief Floor Map of Kunibiki Messe



Brief Map of Shimane Art Museum (Banquet Venue)

