

ISMVL 2025

June 5 – 6, 2025, Montreal, Quebec, Canada

Call for Papers



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The IEEE Computer Society's Technical Committee on Multiple-Valued Logic will hold its 55th annual symposium in Montreal, Quebec, Canada, in June 5 – 6, 2025. You are invited to submit an original paper, survey, or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

Algebra and Formal Aspects
Automatic Reasoning
Logic Programming
Philosophical Aspects of MVL
Fuzzy Logic and Soft Computing
Data Mining
Machine Learning and Robotics
Quantum Computing
Reversible Computing

Logic Design and Switching Theory
Test and Verification
Spectral Techniques
Circuit/Device Implementation
VLSI Architecture and Computing
System-on-Chip Technology
MVL Aspects of Nanotechnology
MVL Aspects of Medical Technologies
MVL Approaches to Big Data

Authors should submit papers in PDF format by ~~November 1~~ **December 15 (last extension), 2024** via the symposium website. Each manuscript should include a 50-to-100 word abstract, and should not exceed 6 pages in the 2-column IEEE Proceedings format. The proceedings of ISMVL are part of Ei Compendex being the broadest and most complete engineering literature database available in the world. Special issues of some journals are planned for selected papers.

Authors will be notified about acceptance by **February 1, 2025**. Camera-ready copies of accepted papers are due on **March 1, 2025**. For submission details, please visit the symposium website: <https://mvl.jp/ISMVL2025/>

The symposium committee is also planning to hold a **special session tribute to Kenneth C. Smith**.

The symposium will be co-located with the 34th International Workshop on Post-Binary ULSI Systems and Reed-Muller 2025 Workshop. Information on accommodations and transportation to the Montreal will be provided at a later date. This event is sponsored by the IEEE Computer Society. For additional information, please contact:

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