

**Journal of Applied Logics (JALs)
Special Issue on Multiple-Valued Logic and Applications**

CALL FOR PAPERS

The [Journal of Applied Logics \(JALs-ifCoLog\)](#) will publish a special issue on Multiple-Valued Logic and Applications. You are invited to submit an article on any subject in the area of multiple-valued logic, including but not limited to:

Circuit/Device Implementation	Multi-bit per Cell Logic & Memory
Multiple-Valued Decision Diagrams	MVL Aspects of Nanotechnology
Quantum Computing	System-on-Chip Technology
Test and Verification	VLSI Architecture and Computing
Brain Scale Integration Circuits and Computing	Reversible, Stochastic Computing
Application of MVL to Medical Technologies, robotics, machine learning	Spectral Techniques
Algebra and Formal Aspects	MVL approaches to Data Mining and Big Data

Articles and topics should be addressed primarily to the diverse readership of *JALs-ifCoLog*. Unpublished and original work is preferred, however extended articles from IEEE ISVML 2023 can be submitted as well with at least 30% of novel content. Articles should target novel and emerging end-applications of multiple-valued logic. Authors should submit papers by using the guidelines and template that can be found at <https://npc.nu.edu.kz/docs/GuideForAuthors.pdf> and <https://npc.nu.edu.kz/docs/myifcolog.cls> respectively. The submission should be sent directly to malu@hiroshima-cu.ac.jp or to behrisch@logic.at. Each manuscript should follow guidelines in the guide for authors. This special issue will follow the schedule below:

September 29, 2023
December 30, 2023
February 10, 2024
February 28, 2024
March 15, 2024
April 2024

Due date for papers
First notification to authors
Due date for revisions
Final notification
Final manuscript due
Special issue publication date

For additional information, please contact members of the editorial team:

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