

Special Issue on Multiple-Valued logic in the Journal of Applied Logics IfCoLog 2024

Description

This year we have the honor to invite you to a special issue on Multiple-Valued Logic (MVL) targeted to novel submissions and extended papers from the ISMVL 2024 conference.

Areas of interest

Multiple Value Logic	Circuits and MVL	Machine Learning and MVL
Design of MVL Circuits	Spectral Techniques	Emergent Computing
MVL Aspects of Nanotechnology	Multiple-Valued Decision Diagrams	Reversible, Stochastic Computing
VLSI Architecture and Computing	Algebra and Formal Aspects	Multiple Valued Logic Circuits
Test and Verification	Systems and MVL	MVL and Biomedical

Brain Scale Integration Circuits and Computing
MVL approaches to Data Mining and Big Data
Quantum Computing System-on-Chip Technology
Circuit/Device Implementation Multi-bit per Cell Logic & Memory

Guide

Articles and topics should be addressed primarily to the diverse readership of JALs-ifCoLog. Unpublished and original work is preferred, however extended articles from IEEE ISVML 2024 can be submitted as well with at least 30% of novel content. Articles should target novel and emerging end-applications of multiple-valued logic.

Authors should submit papers in pdf (compiled latex files) by using the guidelines and template that can be found respectively at <https://npc.nu.edu.kz/docs/GuideForAuthors>.

pdf and <https://npc.nu.edu.kz/docs/myifcolog.cls>. The submission should be sent directly to malu@hiroshima-cu.ac.jp or to yuminaka@gunma-u.ac.jp. Each manuscript should follow guidelines in the guide for authors.

Important Dates

The dates for the different stages of submission are subject to change

1. Submission deadline: August 31 2024
2. Return to authors after review: November 31 2024
3. Final version (with possible additional one round of review) April 30 2025

People

This year guest editors are

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